WEST Refine Search Page 1 of 3

## **Refine Search**

## Search Results -

| Term   | Documents |
|--|-----------|
| CREDIT   | 92761     |
| CREDITS  | 13661     |
| SMART  | 81206     |
| SMARTS   | 383       |
| CARD\$1  | 0         |
| CARD   | 520430    |
| CARDA  | 251       |
| CARDB  | 422       |
| CARDC  | 32        |
| CARDD  | 14        |
| CARDE  | 273       |
| (L27 AND (CREDIT OR SMART) NEAR1<br>CARD\$1).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD. | 5.        |

There are more results than shown above. Click here to view the entire set.

| Database: | US Pre-Grant Publication Full-Te<br>US Patents Full-Text Database<br>US OCR Full-Text Database<br>EPO Abstracts Database<br>JPO Abstracts Database<br>Derwent World Patents Index<br>IBM Technical Disclosure Bulleti |       |               |
|-----------|---|-------|---------------|
| Search:   | L52   |       | Refine Search |
|           | Recall Text   | Clear | Interrupt     |
|           |   |       | <br>          |

## Search History

DATE: Wednesday, August 23, 2006 Purge Queries Printable Copy Create Case

Set Name Query side by side

Hit Set
Count Name
result set

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| DB=        | PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR  |       |            |
|------------|--|-------|------------|
| <u>L52</u> | L27 and (credit or smart) near1 card\$1  | 5     | <u>L52</u> |
| <u>L51</u> | 126 and 114  | 3     | <u>L51</u> |
| <u>L50</u> | 126 and 113  | 0     | <u>L50</u> |
| <u>L49</u> | 126 and 112  | 2     | <u>L49</u> |
| <u>L48</u> | 126 and 111  | 3     | <u>L48</u> |
| <u>L47</u> | 126 and 110  | 42    | <u>L47</u> |
| <u>L46</u> | 126 and 19   | 225   | <u>L46</u> |
| <u>L45</u> | 126 and 18   | 2152  | <u>L45</u> |
| <u>L44</u> | 130 and 114  | 0     | <u>L44</u> |
| <u>L43</u> | 130 and 113  | 0     | <u>L43</u> |
| <u>L42</u> | 130 and 112  | 0     | <u>L42</u> |
| <u>L41</u> | 130 and 111  | 0     | <u>L41</u> |
| <u>L40</u> | 130 and 110  | 0     | <u>L40</u> |
| <u>L39</u> | 130 and 19   | . 0   | <u>L39</u> |
| <u>L38</u> | 130 and 18   | 64    | <u>L38</u> |
| <u>L37</u> | 127 and 114  | 0     | <u>L37</u> |
| <u>L36</u> | 127 and 113  | 0     | <u>L36</u> |
| <u>L35</u> | 127 and 112  | 0     | <u>L35</u> |
| <u>L34</u> | 127 and 111  | 0     | <u>L34</u> |
| <u>L33</u> | 127 and 110  | 1     | <u>L33</u> |
| <u>L32</u> | 127 and 19   | 5     | <u>L32</u> |
| <u>L31</u> | 127 and 18   | 89    | <u>L31</u> |
| <u>L30</u> | L27 and register\$1 near4 (bank\$1 or file\$1 or group\$4)   | 129   | <u>L30</u> |
| <u>L29</u> | L27 and secur\$7   | 28    | <u>L29</u> |
| <u>L28</u> | L27 and smart near1 card\$1  | 3     | <u>L28</u> |
| <u>L27</u> | L26 near25 stack\$1  | 282   | <u>L27</u> |
| <u>L26</u> | (correspond\$4 or repect\$5 or correlat\$4 or associat\$4 or reflect\$5) near10 register\$1 near12 (bit\$1 or flag\$1 or state\$1) | 19761 | <u>L26</u> |
| <u>L25</u> | 115 and stack\$1   | 35    | <u>L25</u> |
| <u>L24</u> | 115 and 19   | 0     | <u>L24</u> |
| <u>L23</u> | 115 and 18   | 2     | <u>L23</u> |
| <u>L22</u> | 11 and 114   | 0     | <u>L22</u> |
| <u>L21</u> | 11 and 113   | 0     | <u>L21</u> |
| <u>L20</u> | 11 and 112   | 1     | <u>L20</u> |
| <u>L19</u> | 11 and 111   | 2     | <u>L19</u> |
| <u>L18</u> | 11 and 110   | 23    | <u>L18</u> |
| <u>L17</u> | 11 and 19  | 87    | <u>L17</u> |
| <u>L16</u> | 11 and 18  | 822   | <u>L16</u> |
| <u>L15</u> | 11 and smart near1 card\$  | 81    | <u>L15</u> |
| DB=        | PGPB,USPT; PLUR=YES; OP=OR   |       |            |
| <u>L14</u> | (713/164)![CCLS]   | 354   | <u>L14</u> |

| <u>L13</u> | (725/6)![CCLS]  | 73    | <u>L13</u> |
|------------|---|-------|------------|
| <u>L12</u> | (902/26)[CCLS]  | 175   | <u>L12</u> |
| <u>L11</u> | (705/64)![CCLS]   | 693   | <u>L11</u> |
| <u>L10</u> | (712/36)[CCLS]  | 280   | <u>L10</u> |
| <u>L9</u>  | (712/32-38)[CCLS]   | 1408  | <u>L9</u>  |
| <u>L8</u>  | (712/2-300)[CCLS]   | 12478 | <u>L8</u>  |
| DB=1       | PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR   |       |            |
| <u>L7</u>  | l2 and register\$1 near4 (bank\$1 or file\$1 or group\$4)   | 25    | <u>L7</u>  |
| <u>L6</u>  | 12 and secur\$7   | 9     | <u>L6</u>  |
| <u>L5</u>  | 12 and smart near1 card\$1  | 2     | <u>L5</u>  |
| <u>L4</u>  | 12 and samart near1 card\$1   | 0     | <u>L4</u>  |
| <u>L3</u>  | L2 and (auxiliary ) near3 register\$1   | 2     | <u>L3</u>  |
| <u>L2</u>  | L1 near18 stack\$1  | 54    | <u>L2</u>  |
| <u>L1</u>  | (correspond\$4 or repect\$5 or correlat\$4 or asssociat\$4) near10 register\$1 near12 (bit\$1 or flag\$1 or state\$1) | 9057  | <u>L1</u>  |

## END OF SEARCH HISTORY